



CE-ATA Technical Errata

Errata ID	Protocol 024
Affected Spec Ver.	Protocol 1.0 and 1.1
Corrected Spec Ver.	

Submission info

Name	Company	Date
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Description of the specification technical flaw (add space as needed)

The specification does not clearly define the allowed MMC commands when an ATA command is outstanding. This erratum clarifies the allowed sequences.

Description of the correction

A new subsection shall be added to the Reduced ATA Command Emulation section as shown:

Allowed MMC Commands when ATA Command Outstanding

While an ATA command is outstanding and prior to a RW_MULTIPLE_BLOCK (CMD61) being issued, only a subset of the MMC command set is required to be accepted by the device.

When nIEN=1, the device is only required to accept the following MMC commands:

- RW_MULTIPLE_BLOCK (CMD61) to transfer data
- FAST_IO (CMD39) to read ATA taskfile registers.
- GO_IDLE_STATE (CMD0) or STOP_TRANSMISSION (CMD12) to reset the device

When nIEN=0, the device is only required to accept the following MMC commands:

- RW_MULTIPLE_BLOCK (CMD61) to transfer data or arm the interrupt
- GO_IDLE_STATE (CMD0) or STOP_TRANSMISSION (CMD12) to reset the device

Disposition log

5/28/2006	Erratum captured
5/31/2006	Updates to reflect these are the commands the device is required to accept
7/17/2006	Erratum ratified

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